JF W

**PATENT** 



Docket No.: 050024-0026

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277

Seiichi BANBA, et al. : Confirmation Number: 5193

Application No.: 10/802,878 : Group Art Unit: 2817

Filed: March 18, 2004 : Examiner: P. Nguyen

For: VARIABLE IMPEDANCE CIRCUIT, VARIABLE GAIN DIFFERENTIAL AMPLIFIER,

MULTIPLIER, HIGH-FREQUENCY CIRCUIT AND DIFFERENTIAL DISTRIBUTED

**AMPLIFIER** 

## RESPONSE TO RESTRICTION REQUIREMENT

Mail Stop Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In the Office Action dated December 15, 2005, the Examiner imposed a Restriction Requirement pursuant to 35 U.S.C. §121, requiring election of either:

Group I. Claims 1 through 11 directed to a combination; or

Group II. Claims 12 through 19 directed to subcombination high frequency circuit.

In response, Applicants elect to prosecute combination claims 1 through 11 (Group I). Favorable consideration of claims 1 through 11 is solicited.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this paper,

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including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP

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Facsimile: 202.756.8087 **Date: January 12, 2006** 

Please recognize our Customer No. 20277 as our correspondence address.